

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventors: Osamu ICHIKAWA

Application No.: New Patent Application

Filed: March 19, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND TEST METHOD
THEREOF

INFORMATION DISCLOSURE STATEMENT

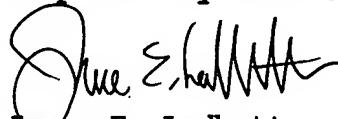
Honorable Commissioner of
Patents and Trademarks
Washington, DC 20231

Sir:

Pursuant to Rules 56 and 99, Applicants hereby call the attention of the Patent Office to the reference(s) listed on the attached Form PTO 1449.

Applicants present this art so that the Patent Office may, in the first instance, determine any relevancy thereof to the presently claimed invention, see Beckman Instruments, Inc. v. Chemtronics, Inc., 439 F.2d 1369, 1380, 165 USPQ 355, 364 (5th Cir. 1970). Also see Patent Office Rules 104 and 106. Applicants respectfully request that this art be expressly considered during the prosecution of this application and made of record herein and appear among the "References Cited" on any patent to issue herefrom.

Respectfully submitted,



James E. Ledbetter
Registration No. 28,732

Date: March 19, 2004

JEL/apg
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

ATTY. DOCKET NO.

L8462.04111

SERIAL NO.

Unassigned

APPLICANT

Osamu ICHIKAWA

FILING DATE

DATE
March 19, 2004

GROUP

Unassigned

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

[illegible]

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.